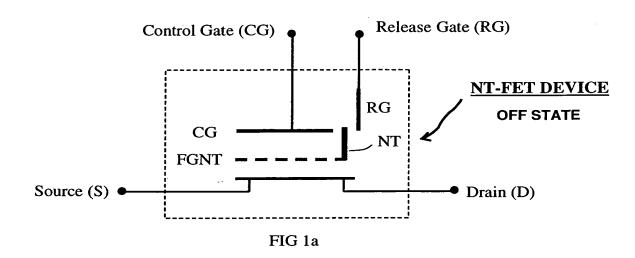
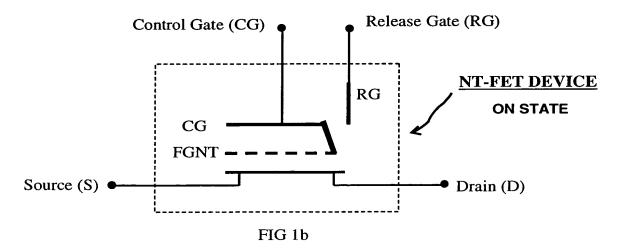


REPLACEMENT SHEET
Appl. No. 10/811,191
Filing Date: March 26, 2004
Inventor: Thomas Rueckes et al.





REPLACEMENT SHEET Appl. No. 10/811,191 Filing Date: March 26, 2004

Inventor: Thomas Rueckes et al.

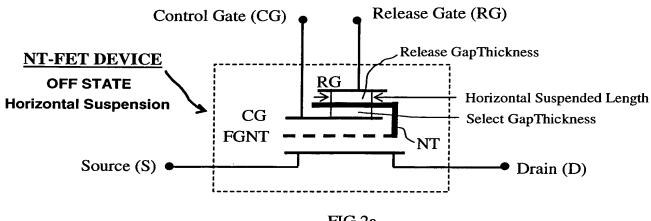
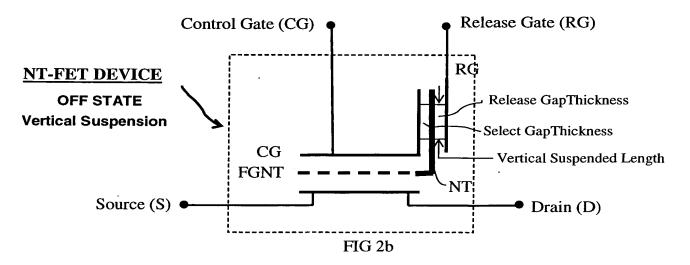


FIG 2a



REPLACEMENT SHEET-Appl. No. 10/811,191 Filing Date: March 26, 2004

Inventor: Thomas Rueckes et al.

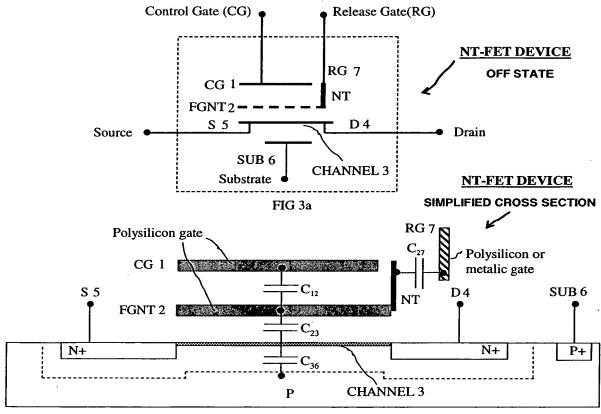
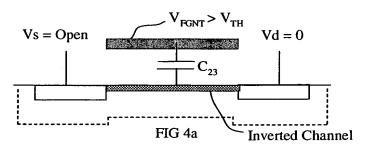


FIG 3b

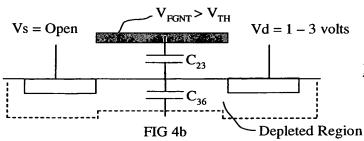
REPLACEMENT SHEET Appl. No. 10/811,191 Filing Date: March 26, 2004 Inventor: Thomas Rueckes et al.

4/11

CHANNEL REGION MODULATION



Inverted Channel: electrons at V = 0 volts

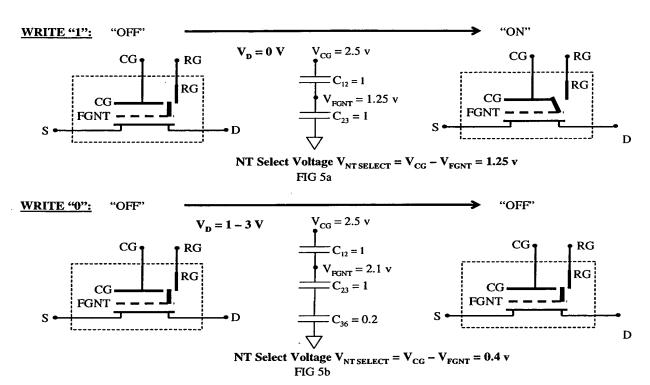


Depleted Channel: electrons flow to positive terminal

REPLACEMENT SHEET Appl. No. 10/811,191

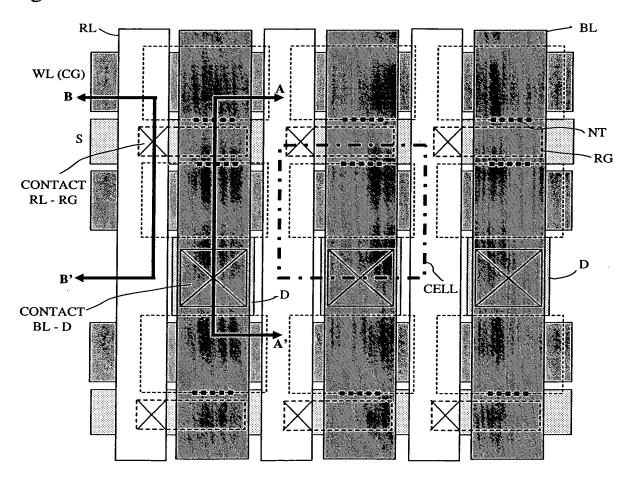
Filing Date: March 26, 2004 Inventor: Thomas Rueckes et al.

5/11

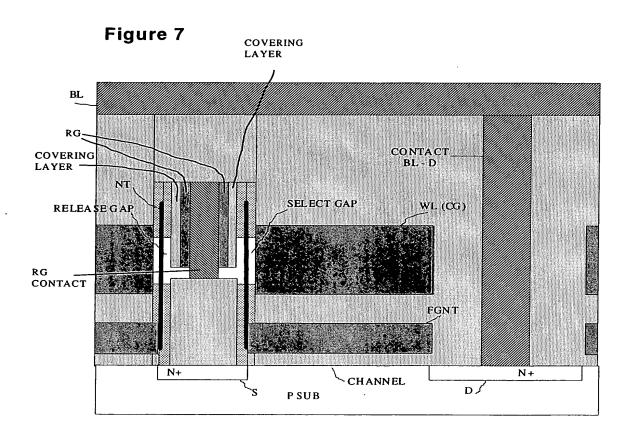


NET: SELECT VOLTAGE THRESHOLD RANGE > 0.4 v & < 1.25 v

Figure 6



7/11



REPLACEMENT SHEET
Appl. No. 10/811,191
Filing Date: March 26, 2004
Inventor: Thomas Rueckes et al.

Figure 8

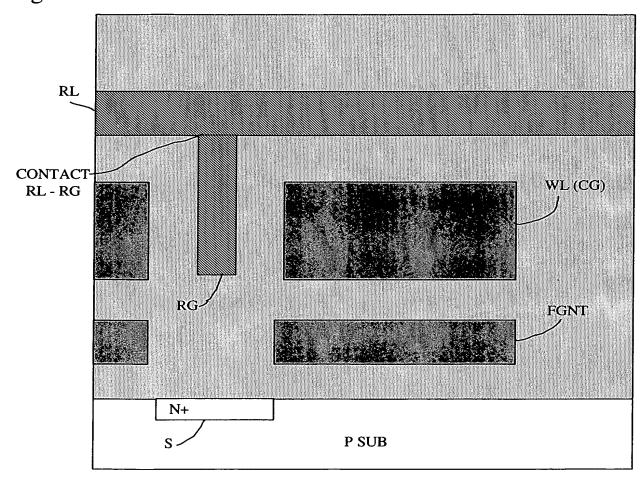
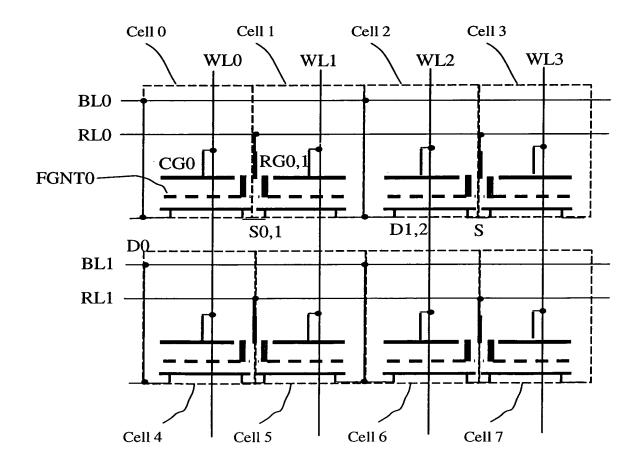


Figure 9



10/11

Figure 10

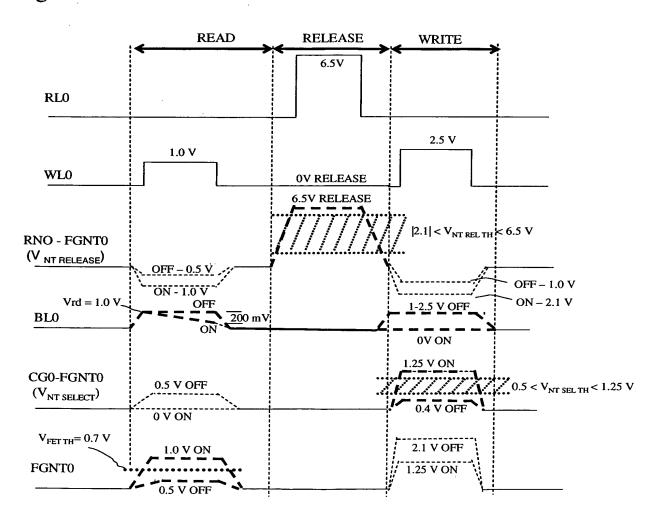


Figure 11

